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K. P. M. S.
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501001-A-01-US (Singh)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): N. Singh et al.
Case: 501001-A-01-US (Singh)
Serial No.: 09/924,983
Filing Date: August 8, 2001
Group: To Be Assigned
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Signature: Joseph B. Ryan Date: August 29, 2001

Title: Fault Tolerance Software System with Periodic
External Self-Test Failure Detection

TRANSMITTAL OF FORMAL DRAWINGS

Assistant Commissioner for Patents
Washington, D.C. 20231

Attention: Official Draftsperson

Sir:

Applicants submit herewith seven (7) sheets of formal drawings in the above-referenced application.

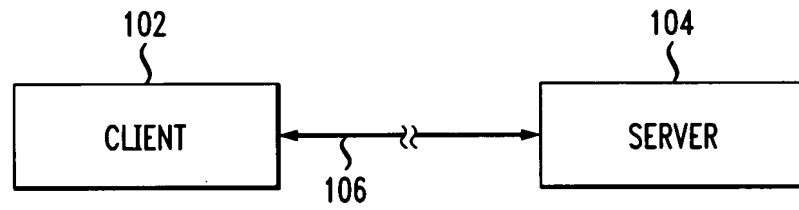
Respectfully submitted,

Date: August 29, 2001

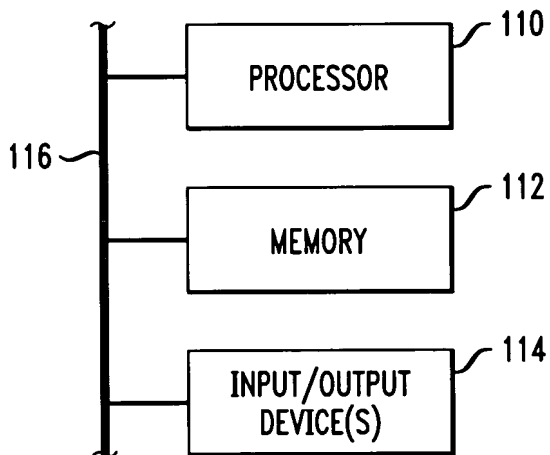
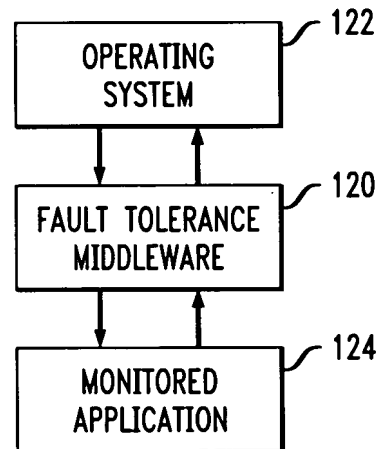
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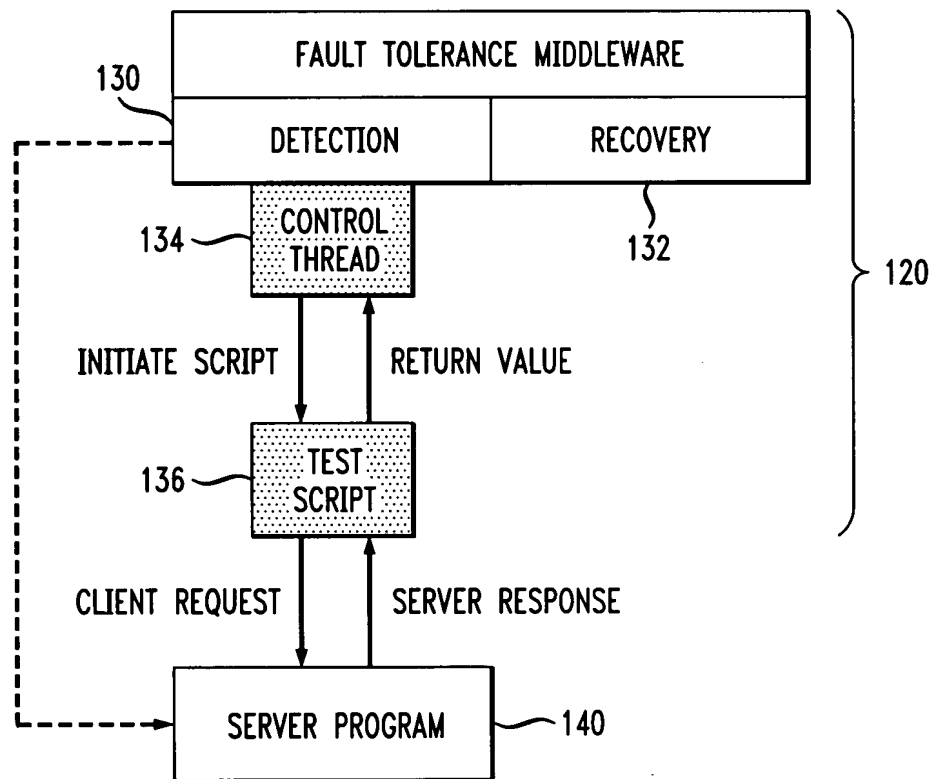
*FIG. 1*100

102, 104

FIG. 2*FIG. 3*

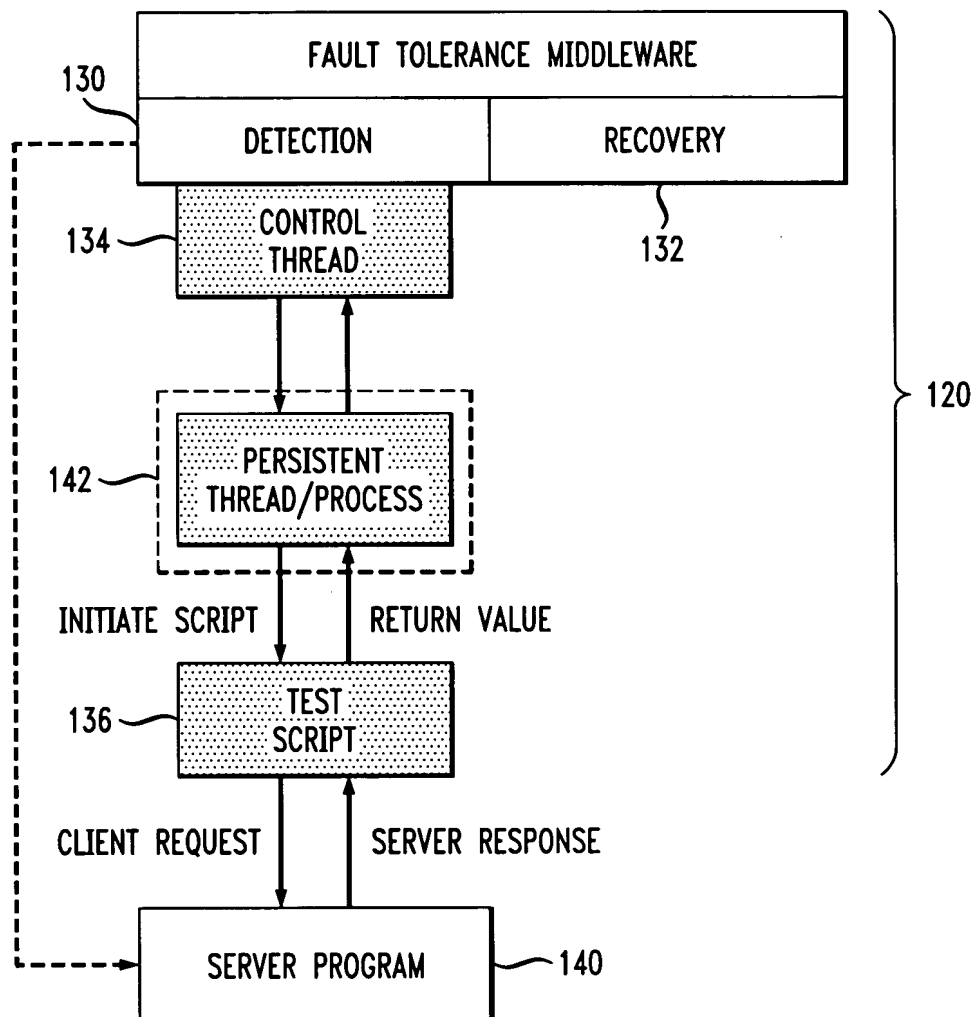
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FIG. 4A



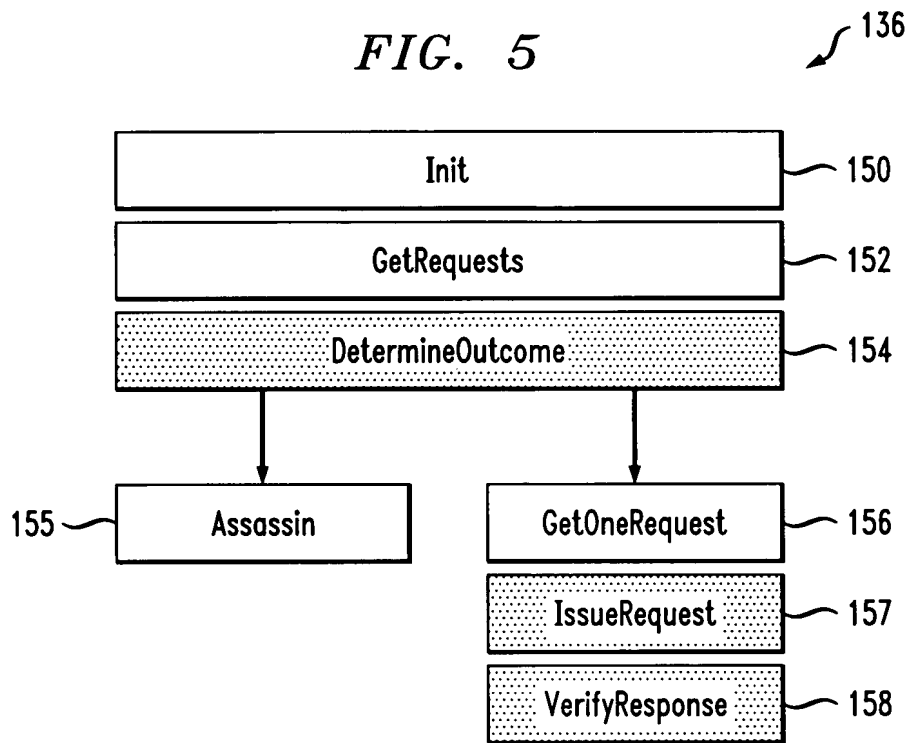
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FIG. 4B



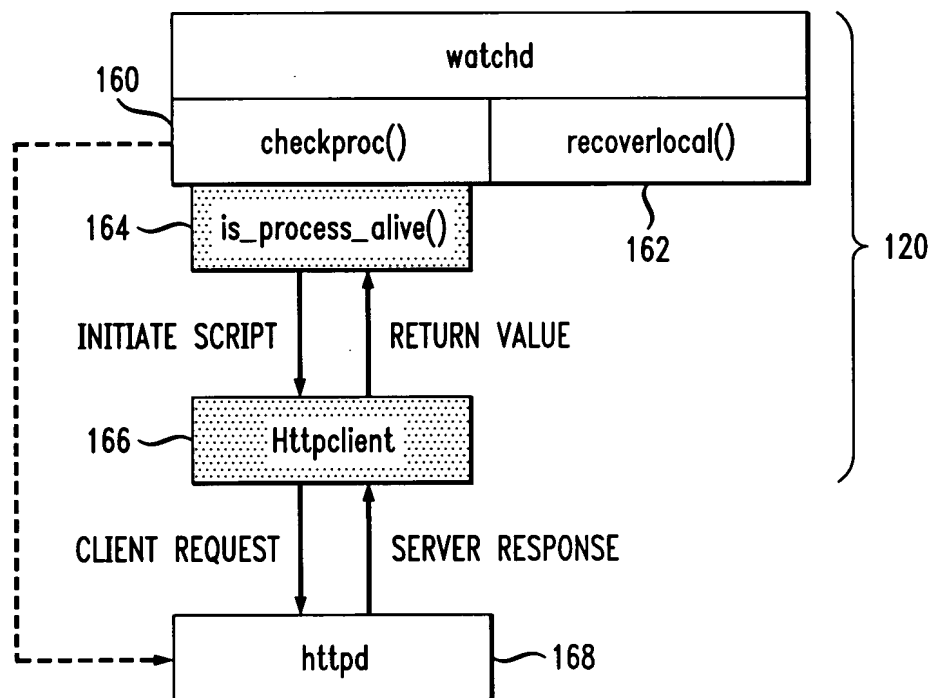
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FIG. 5



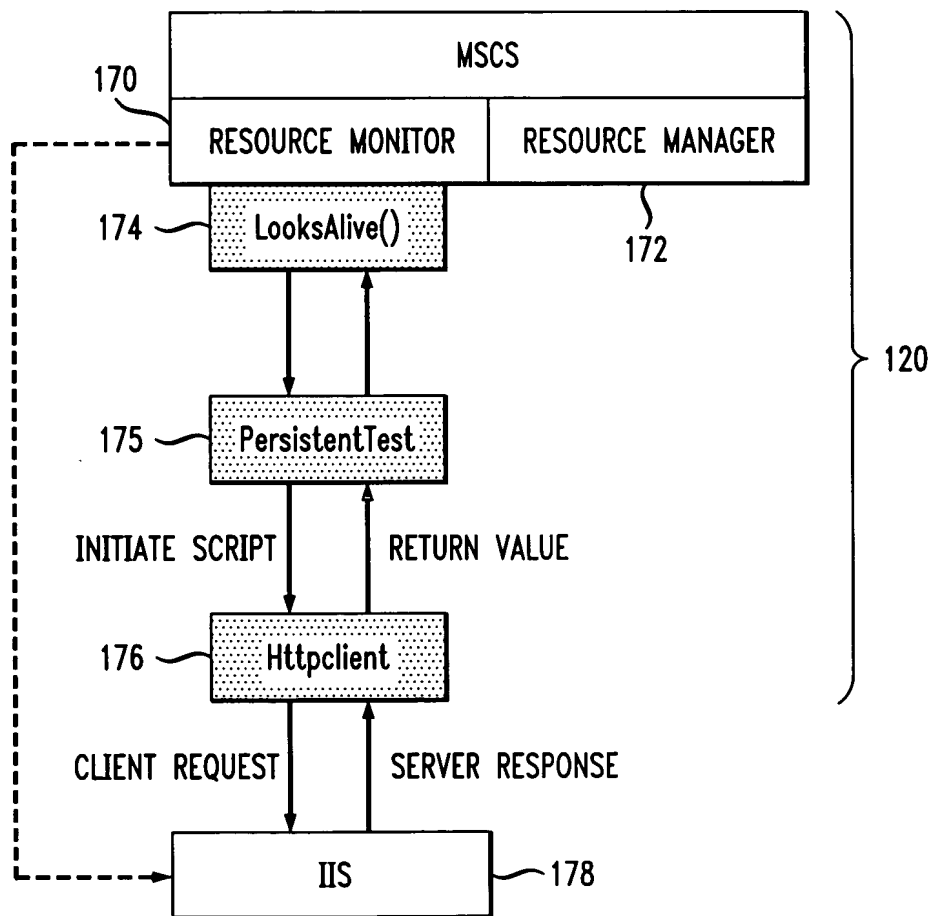
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FIG. 6



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FIG. 7



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FIG. 8

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